



- L1: (10) (HATANAKA near2 ICHIRO).in.
- L2: (150) (SHIBAYAMA near2 AKINORI).in.
- L3: (17) (YAMAGAMI near2 YOSHINOBU).in.
- L4: (20396) (redundan\$2 or replac\$6 or spar\$6 or substitut\$6) adj3 cell
- L5: (6876) (redundan\$2 or replac\$6 or spar\$6 or substitut\$6) adj3 row
- L9: (1639) (redundan\$2 or replac\$6 or spar\$6 or substitut\$6) adj3 signal adj2 generat\$4
- L10: (19964) row adj2 decod\$4
- L11: (62) 9 same 10
- L14: (7) 11 same arrang\$6
- L13: (6) 11 same dispos\$6
- L12: (3) 11 same locat\$4
- L17: (49) 11 not (12 or 13 or 14)
- L24: (80) repair\$4 adj2 signal adj2 generat\$4
- L26: (26) 10 and 24
- L27: (198) (arrang\$6 or dispos\$6 or locat\$6) same 9
- L29: (31) 10 and 27
- L32: (1871) (arrang\$6 or dispos\$6 or locat\$6) same 5
- L33: (368) 10 same 32
- L35: (29) 33 same (opposite or outside)
- L38: (29298) (arrang\$6 or dispos\$6 or locat\$6) same fuse
- L39: (211) 38 same 10
- L41: (6592) 365/200
- L42: (2277) 365/61
- L43: (3146) 365/03
- L44: (30730) 27 or 32 or 38
- L45: (1476) 44 and 41
- L47: (186) 44 and 43
- L46: (168) 44 and 42
- L50: (3718) 38 same (opposite or outside)
- L48: (9) 27 same (opposite or outside)
- L49: (113) 32 same (opposite or outside)
- L61: (12) 50 same 10
- L63: (478) 44 same 10
- L64: (260) 63 and 41
- L66: (28) 63 and 42

• Failed

Search

DB: US-PGPUB;USPAT;EPO;JPO;DERVENT;IBM... Bulk
 Highlight all hit terms initially

Default operator: OR

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	2	3	4
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6366939 A	19941011	15	Row redundancy circuit of a se	365/200	714/710		Oh; Seung-Cheol et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>				
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6479371 A	19951226	31	Semiconductor memory device	365/200	327/526;		Ootani; Takayuki	<input checked="" type="checkbox"/>	<input type="checkbox"/>				

Has: Details HTML

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